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September 9, 2004

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Fr:

George O. Saile, Reg. No. 19,572

28 Davis Avenue

Poughkeepsie, N.Y. 12603

Subject:	Γ	Serial No.	10/697,925	10/30/03
	. •	Seiki Ogura		
	·	Twin NAND Device Structure, Array Operations and Fabrication Method		•
	L	Grp. Art Unit:		
	Pi	reliminary An	nendment	

Dear Sir:

Please amend the above-identified application for patent as follows:

The Commissioner is hereby authorized to charge payment of any additional fee involved with added Claims and the like to Deposit Account No. 19-0033.

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on September /4 2004.

Name <u>Stephen B. Ackerman</u>	<u>Reg. No. 37761</u>				
Signature 52					
Date September 4, 2004					
00010111001					

Please amend the above-identified application as follows:

Amendments to the Specification begin on page 3 of this paper.

Amendments to the Drawings begin on page 8 of this paper and include both an attached replacement sheet and an annotated sheet showing changes.

Remarks/Arguments begin on page 9 of this paper.

An **Appendix** including amended drawing figures is attached following page 10 of this paper.